

A3995: DMOS Dual Full Bridge PWM Motor Driver

The A3995 is designed to drive two dc motors at currents up to 2.4 A. Capable of drive voltages up to 36 V, the A3995 includes two independent fixed off-time PWM current regulators that operate in either fast or slow decay mode, as determined by the MODE input. Internal synchronous rectification control circuitry is provided to improve power dissipation during PWM operation.

Protection features include: thermal shutdown with hysteresis, undervoltage lockout (UVLO) and crossover current protection. Special power-up sequencing is not required.

The A3995 is supplied in a 36 pin QFN package (suffix EV) with exposed power tab for enhanced thermal performance. It has a 6 mm × 6 mm footprint, with a nominal overall package height of 0.90 mm, and is lead (Pb) free, with 100% matte tin leadframe plating.